

# uno 10 GX 10M Prototyping System

## Product Summary

The proFPGA uno 10 GX 10M system is a complete and modular FPGA solution, which meets highest requirements in the area of FPGA based Prototyping. It addresses customers who need a scalable and flexible high speed ASIC Prototyping and IP verification solution for early software development and real time system verification. The innovative system concept offers highest flexibility and reusability reconfigurability for several projects, which guarantees the best return on investment.

## Highest Flexibility

The system architecture is based on a modular system concept. The FPGAs are assembled on dedicated single-chip FPGA modules, which will be plugged on the proFPGA uno, duo or quad motherboard. This offers the highest flexibility to use for example different FPGA types on the same motherboard. The user has access to almost all I/Os of the FPGA, which gives him maximum freedom regarding the FPGA interconnection structure. This way the prototyping system can be adapted in the best way to any user design. Furthermore the system offers a total of 16 extension sites on the top and bottom site for standard or user specific extension boards like DDR4 memory, PCIe gen1/2/3/4, Gigabit Ethernet, USB 3.0 or other high speed interfaces.

## Maximum Performance

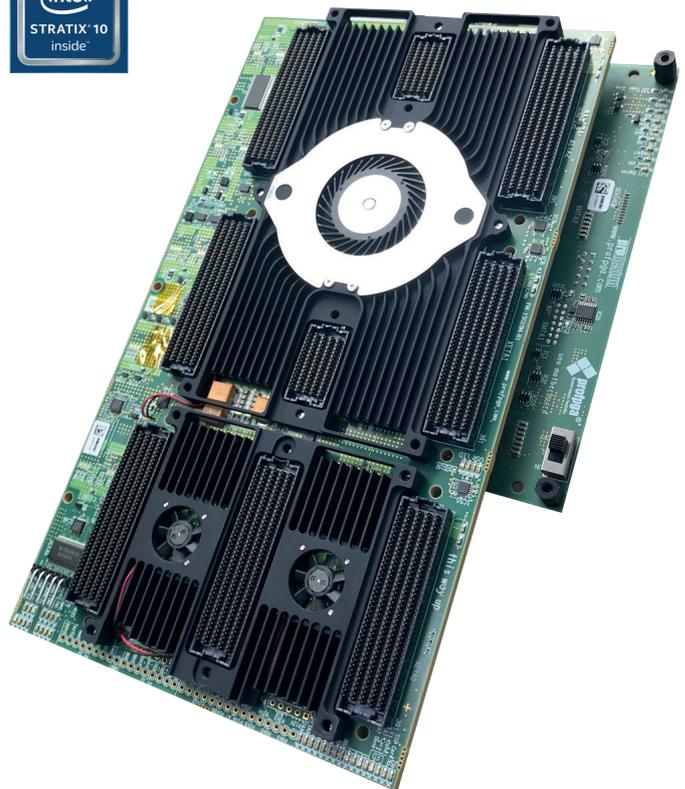
The well designed boards of the proFPGA system are optimized and trimmed to guarantee best signal integrity and to achieve highest performance. The high speed boards together with specific high speed connectors allow a maximum point-to-point speed of up to 1.0 Gbps single ended over the standard FPGA I/Os and up to 17.4 Gbps differential over the high speed serial transceivers of the FPGA. This performance allows the designer to run his design at the highest possible speed on the proFPGA system.

## Biggest Capacity

Equipped with one Intel® Stratix® 10 GX 10M FPGA module, the proFPGA uno system can handle up to 60 M ASIC gates on only one board.

## Very User Friendly

The proFPGA prototyping system provides an extensive set of features and tools, like remote system configuration, integrated self and performance test, automatic board detection, automatic I/O voltage programming, system scan and safety mechanism, which simplifies the usage of the FPGA based system tremendously.



## Key Features

- 60 M ASIC gates capacity on one board
- 2076 user I/Os
- 48 dedicated high speed serial transceivers
- 16 individually adjustable voltage regions
- Up to 1.0 Gbps single ended point-to-point speed
- Up to 17.4 Gbps with high speed transceivers
- Smart Stacking Technology (SST)
- High performance host interface (DMBI)
- proFPGA Builder Software

## Innovative Technologies



### Smart Stacking Technology

- ❖ Board detection when boards are plugged
- ❖ Automatic and right I/O voltage setting and programming with conflict detection
- ❖ Integrated interconnection self and performance test
- ❖ Smart I/O resource management. No I/O resources get lost or blocked by connectors



### FPGA Mixing Technology

- ❖ Easy plugging and unplugging of FPGA modules on motherboards
- ❖ Various FPGAs from different vendors can be mixed
- ❖ Automatic scan and detection of FPGA modules, when plugged
- ❖ Different FPGA configurations are controlled by proFPGA Builder software



### Device Message Box Interface

- ❖ High speed, low latency data exchange system
- ❖ Enables various use modes and functionalities like remote system configuration and monitoring, debug, application level programming and co-simulation
- ❖ Up to 3.5 Gbps data transfer rate
- ❖ Runs over USB, Ethernet or PCIe

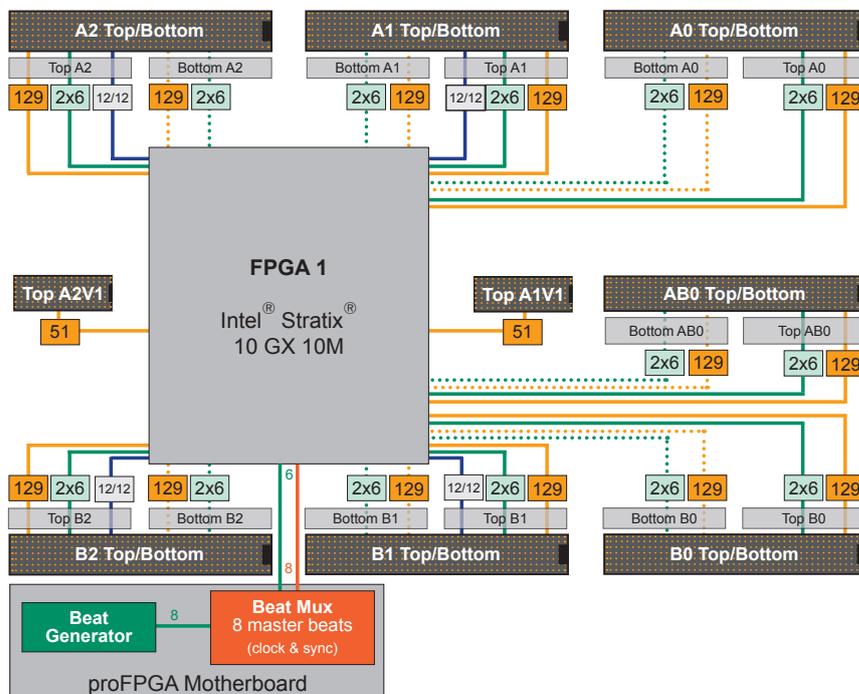


### Advanced Clock Management

- ❖ 8 fixed clocks
- ❖ Run-time configurable local clocks
- ❖ Each clock with sync signals

profpga uno 10 GX 10M Specification	
FPGA	- 1 x Intel Stratix 10 GX 10M FPGA Modules
Capacity	- 60 M ASIC gates on one board
FPGA-internal memory	- M20K: 253 Mbit / MLAB: 55 Mbit
Signaling rate	- Up to 1.0 Gbps single ended (standard I/O)/ up to 17.4 Gbps differential
Extension sites	- 16 Extension sites with high speed connectors
I/O resources	- Overall 2076 signals for I/O and inter FPGA connection - 7 x 141 I/Os to top side connectors - 2 x 51 I/Os to top side connectors - 1 x 141 I/Os to bottom side connectors
High speed I/O transceivers	- 48 dedicated transceivers running up to 17.4 Gbps - 4 x 12 transceivers to top side connectors
Available interface boards	- USB 3.0, PCIe Gen2/Gen3, MIPI, DVI, DDR3 memory, Gb Ethernet, etc.
Voltage regions	- 16 individually adjustable I/O voltage regions - Stepless from 1.2V up 1.8V on 6 extension sites - Automated detection of daughter cards and setting of right voltages
Clocking	- 8 fixed global clock and sync inputs - use of up to 2 x 6 clock-capable I/Os per connector
Configuration	- From host via Ethernet, USB or standalone over USB stick or Xilinx JTAG
Data exchange	- On board DMBI (Device Message Box Interface) - Data exchange rate: - Ethernet (up to 100 Mbps), USB (480 Mbps)
Power	External (optional) ATX Power Supply (12 V, 24 - 35 A output)
Dimensions	- 5.91" x 0.95" x 5.91" / 150 mm x 24 mm x 150 mm (width x height x depth) - 0.5 kg weight

### profpga uno 10 GX 10M I/O and Clock Architecture



Signal Types	
<span style="color: orange;">—</span>	IO
<span style="color: green;">—</span>	IO/Clock
<span style="color: blue;">—</span>	Transceivers